

PRINTER RUSH

(PTO ASSISTANCE)

FFW

Application : 10/047928

Examiner : Phu

GAU : 2631

From: TW

Location: IDC FMF FDO

Date: 7-17-05

Tracking #: 6098295

Week Date: 4-25-05

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449		<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM		<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW		<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW	<u>6-10-05</u>	<input checked="" type="checkbox"/> Other - NOA
<input type="checkbox"/> DRW		
<input type="checkbox"/> OATH		
<input type="checkbox"/> 312		
<input type="checkbox"/> SPEC		

ATTN: Chief Draftsperson

[RUSH] MESSAGE:

The NOA (examiner's amendment) dated 6-10-05 states that new drawings must be submitted to add the ~~code~~ label "Price Act" to Figures 1-4b but there were no new drawings located in the file.

Please verify

Thank You

Tal

[XRUSH] RESPONSE:

CORRECTED DRAWINGS
ATTACHED

RESOLVED

7-21-2005

INITIALS: JSJ

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Herry Skillman on 6/7/05.

The application has been amended as follows:

IN THE ABSTRACT:

- The abstract is replaced as following:

ABSTRACT

Disclosed is a device for recovering a burst-mode clock. The burst-mode clock recovery device includes a delay unit and a logic element. A reference clock is produced by implementing a logic operation with respect to a signal output from a signal forming device and a signal output from the signal forming device and delayed by a delay unit. A duty of a final output signal is corrected by implementing and AND operation with respect to the generated reference clock and an output from a feedback device.

IN THE DRAWINGS:

- In figure 1, a label "PRIOR ART" is added to the figure.
- In figure 2, a label "PRIOR ART" is added to the figure.
- In figure 3, a label "PRIOR ART" is added to the figure.
- In figure 4A, a label "PRIOR ART" is added to the figure.

FIG. 1
PRIOR ART

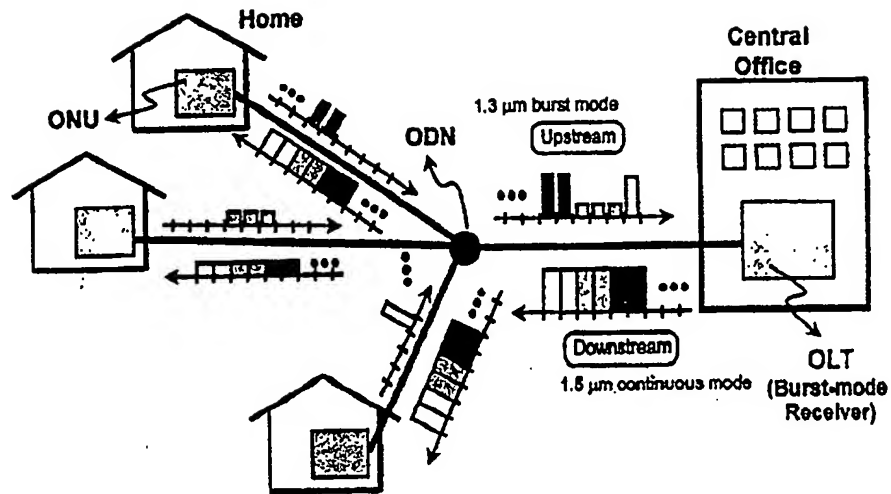


FIG. 2
PRIOR ART

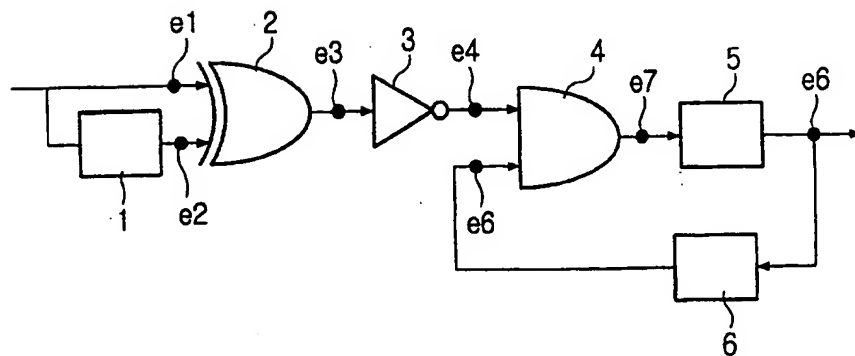


FIG. 3 PRIOR ART

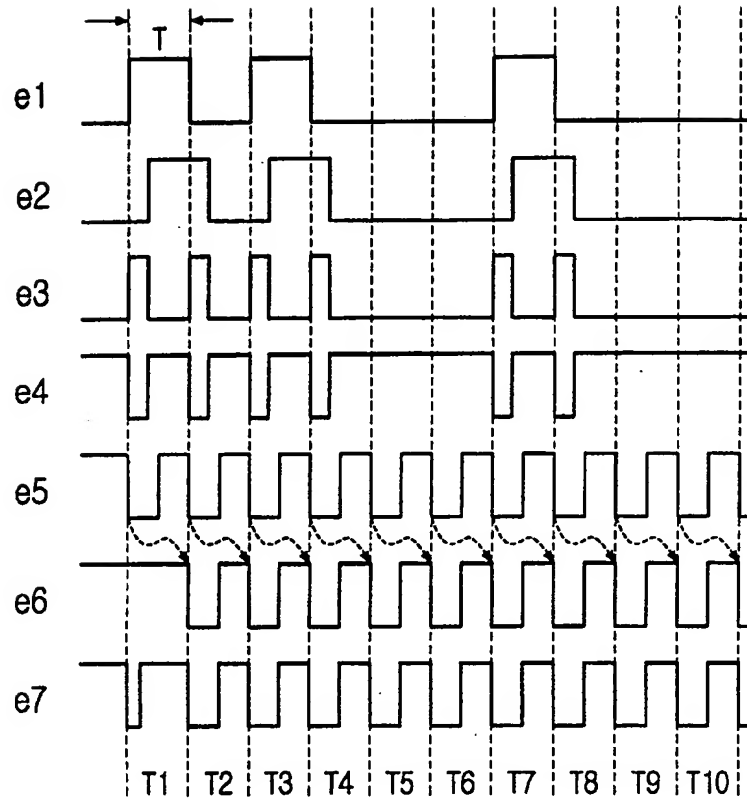


FIG. 4A PRIOR ART

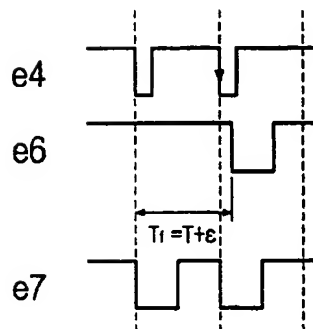


FIG. 4B

